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B. CLAIMS:

1. (currently withdrawn) A configurable interface controller, comprising:
a common component bus;

a plurality of individual controller buses that are adapted to interface with a plurality of individual interface controllers; and

adaptation logic to dynamically connect the common component bus to the individual controller buses.
2. (currently withdrawn) The configurable interface controller as described in claim 1 wherein the common component bus includes a plurality of interface pins.
3. (currently withdrawn) The configurable interface controller as described in claim 2 wherein the adaptation logic further includes:
assignment logic that is adapted to assign a first interface pin from the plurality of interface pins to a first individual controller from the plurality of individual interface controllers.
4. (currently withdrawn) The configurable interface controller as described in claim 3 wherein the assignment logic is adapted to re-assign the first interface pin to a second individual controller from the plurality of individual interface controllers.
5. (currently withdrawn) The configurable interface controller as described in claim 1 wherein the common component bus, the plurality of individual controller buses, and the adaptation logic are included on a single substrate.
6. (currently withdrawn) The configurable interface controller as described in claim 5 wherein the common component bus is adapted to interface with one or more peripheral devices external to the single substrate.

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7. (currently withdrawn) The configurable interface controller as described in claim 1 wherein the adaptation logic is adapted to dynamically connect the common component bus to the individual controller buses during system initialization.
8. (original) A method for dynamically assigning interface pins, said method comprising:
receiving a first assignment request;
identifying one or more interface pins that correspond to the first assignment request;
selecting a first interface controller from a plurality of interface controllers that correspond to the first assignment request; and
associating the identified interface pins with the selected interface controller.
9. (original) The method as described in claim 8 wherein the identified interface pins are selected from the group consisting of an input interface pin and an output interface pin.
10. (original) The method as described in claim 8 further comprising:
receiving a second assignment request, the second assignment request corresponding to the identified interface pins;
selecting a second interface controller from the plurality of interface controllers that correspond to the second assignment request; and
re-associating the identified interface pins to the second interface controller.
11. (original) The method as described in claim 8 wherein the associating is performed using a look-up table.
12. (original) The method as described in claim 8 further comprising:
determining whether there are more interface pins that are not associated with the first interface controller; and
assigning the non-associated interface pins to a second interface controller in response to the determination.

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13. (original) The method as described in claim 8 further comprising:
receiving data from the identified interface pins; and
providing the data to the first interface controller.
14. (original) The method as described in claim 8 wherein the associating is performed at system initialization.
15. (currently withdrawn) An information handling system comprising:
one or more processors;
one or more interface pins;
a plurality of interface controllers;
a memory accessible by the processors;
one or more nonvolatile storage devices accessible by the processors; and
an interface pin assignment tool for assigning one or more of the interface pins to one of the interface controllers, the interface pin assignment tool including:
means for receiving a first assignment request;
means for identifying one or more of the interface pins that correspond to the first assignment request;
means for selecting a first interface controller from the plurality of interface controllers that correspond to the first assignment request; and
means for associating the identified interface pins with the selected interface controller.
16. (currently withdrawn) The information handling system as described in claim 15 wherein the identified interface pins are selected from the group consisting of an input interface pin and an output interface pin.

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17. (currently withdrawn) The information handling system as described in claim 15 further comprising:
means for receiving a second assignment request, the second assignment request corresponding to the identified interface pins;
means for selecting a second interface controller from the plurality of interface controllers that correspond to the second assignment request; and
means for re-associating the identified interface pins to the second interface controller.
18. (currently withdrawn) The information handling system as described in claim 15 wherein the associating is performed using a look-up table.
19. (currently withdrawn) The information handling system as described in claim 15 further comprising:
means for determining whether there are more interface pins that are not associated with the first interface controller; and
means for assigning the non-associated interface pins to a second interface controller in response to the determination.
20. (currently withdrawn) The information handling system as described in claim 15 further comprising:
means for receiving data from the identified interface pins; and
means for providing the data to the first interface controller.
21. (original) A computer program product stored on a computer operable media for dynamically changing pin to interface controller assignment:
means for receiving a first assignment request;
means for identifying one or more interface pins that correspond to the first assignment request;

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means for selecting a first interface controller from a plurality of interface controllers that correspond to the first assignment request; and

means for associating the identified interface pins with the selected interface controller.

22. (original) The computer program product as described in claim 21 wherein the identified interface pins are selected from the group consisting of an input interface pin and an output interface pin.
23. (original) The computer program product as described in claim 21 further comprising:
means for receiving a second assignment request, the second assignment request corresponding to the identified interface pins;
means for selecting a second interface controller from the plurality of interface controllers that correspond to the second assignment request; and
means for re-associating the identified interface pins to the second interface controller.
24. (original) The computer program product as described in claim 21 wherein the associating is performed using a look-up table.
25. (original) The computer program product as described in claim 21 further comprising:
means for determining whether there are more interface pins that are not associated with the first interface controller; and
means for assigning the non-associated interface pins to a second interface controller in response to the determination.
26. (original) The computer program product as described in claim 21 further comprising:
means for receiving data from the identified interface pins; and
means for providing the data to the first interface controller.
27. (original) The computer program product as described in claim 21 wherein the associating is performed at system initialization.